

**Inventor Name Search Result**

Your Search was:

Last Name = SANE

First Name = ANIRUDDHA

Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name	Invent Name
10411632	7380114	20040098577	150	04/11/2003	INTEGRATED CIRCUIT WITH DMA MODULE FOR LOADING PORTIONS OF CODE TO A CODE MEMORY FOR EXECUTION BY A HOST PROCESSOR THAT CONTROLS A VIDEO DECODER	YANCHUS III,PAUL	SANE, ANIRU
10414580	7174358	20040098441	150	04/15/2003	SYSTEM, METHOD, AND APPARATUS FOR DIVISION COUPLED WITH TRUNCATION OF SIGNED BINARY NUMBERS	MALZAHN,DAVID	SANE, ANIRU
10414842	7165086	20040098442	150	04/15/2003	SYSTEM, METHOD, AND APPARATUS FOR DIVISION COUPLED WITH ROUNDING OF SIGNED BINARY NUMBERS	MALZAHN,DAVID	SANE, ANIRU

<u>10606145</u>	<u>6867715</u>	<u>20040263364</u>	150	06/25/2003	SYSTEM, METHOD, AND APPARATUS FOR VARIABLE LENGTH DECODER	NGUYEN,JOHN	SANE. ANIRI
<u>10765809</u>	Not Issued	<u>20050163222</u>	61	01/27/2004	Decoding of digital video standard material during variable length decoding	WONG,ALLEN	SANE. ANIRI
<u>10775652</u>	Not Issued	<u>20050175106</u>	71	02/09/2004	Unified decoder architecture	RASHIDIAN,MOHAMMAD	SANE. ANIRI
<u>11042221</u>	<u>6995696</u>	<u>20050128109</u>	150	01/25/2005	SYSTEM, METHOD, AND APPARATUS FOR VARIABLE LENGTH DECODER	NGUYEN,JOHN	SANE. ANIRI
<u>11051300</u>	Not Issued	<u>20050174493</u>	41	02/04/2005	Reprogramming of select registers in a linked list	CHOE,YONG	SANE. ANIRI
<u>12106437</u>	Not Issued	<u>20090113177</u>	20	04/21/2008	INTEGRATED CIRCUIT WITH DMA MODULE FOR LOADING PORTIONS OF CODE TO A CODE MEMORY FOR EXECUTION BY A HOST PROCESSOR THAT CONTROLS A VIDEO DECODER		SANE. ANIRI
<u>60426504</u>	Not Issued		159	11/15/2002	Algorithm for division coupled with truncation of signed binary numbers		SANE. ANIRI
<u>60426583</u>	Not Issued		159	11/15/2002	Dynamic booting of		SANE. ANIRI

					processor code memory using special "wait" instructions		
60426618	Not Issued		159	11/15/2002	Algorithm for division coupled with rounding of signed binary numbers		SANE. ANIRU
60542594	Not Issued		159	02/05/2004	Reprogramming of select registers in a linked list		SANE. ANIRU

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